

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD AND APPARATUS FOR SYNTHESIZING A HARDWARE SYSTEM FROM A SOFTWARE DESCRIPTION				
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Robert Marc Zeidman</p> <p>Confirmation Number:</p> <p>Attorney Docket Number:</p>					
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Submitted By:	Elec. Sign.				
Robert Marc Zeidman	RZ Inventor				

Documents being submitted:	Files
us-assignment	SynthSys-usassn.xml us-assignment.xsl us-assignment.dtd SynthSys assignment letter (signed) p1.tif SynthSys assignment letter (signed) p2.tif SynthSys assignment letter (signed) p3.tif
us-request	SynthSys-usrequ.xml us-request.dtd us-request.xsl
us-declaration	SynthSys-usdecl.xml us-declaration.dtd us-declaration.xsl
us-fee-sheet	SynthSys-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	SynthSys-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fig1.tif fig2.tif fig3.tif fig4.tif fig5.tif fig6.tif

fig7.tif

fig8.tif

fig9.tif

Comments